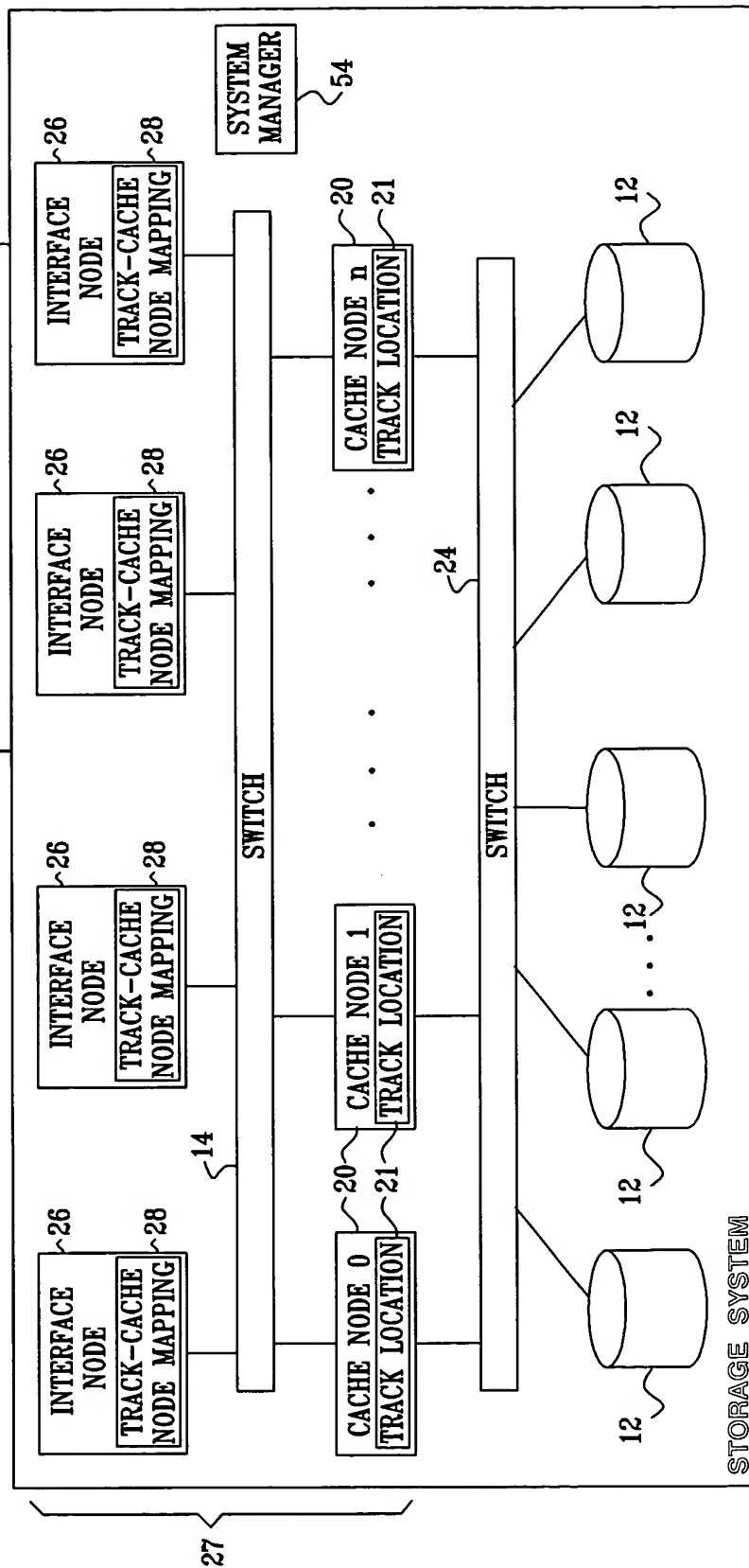


FIG. 1



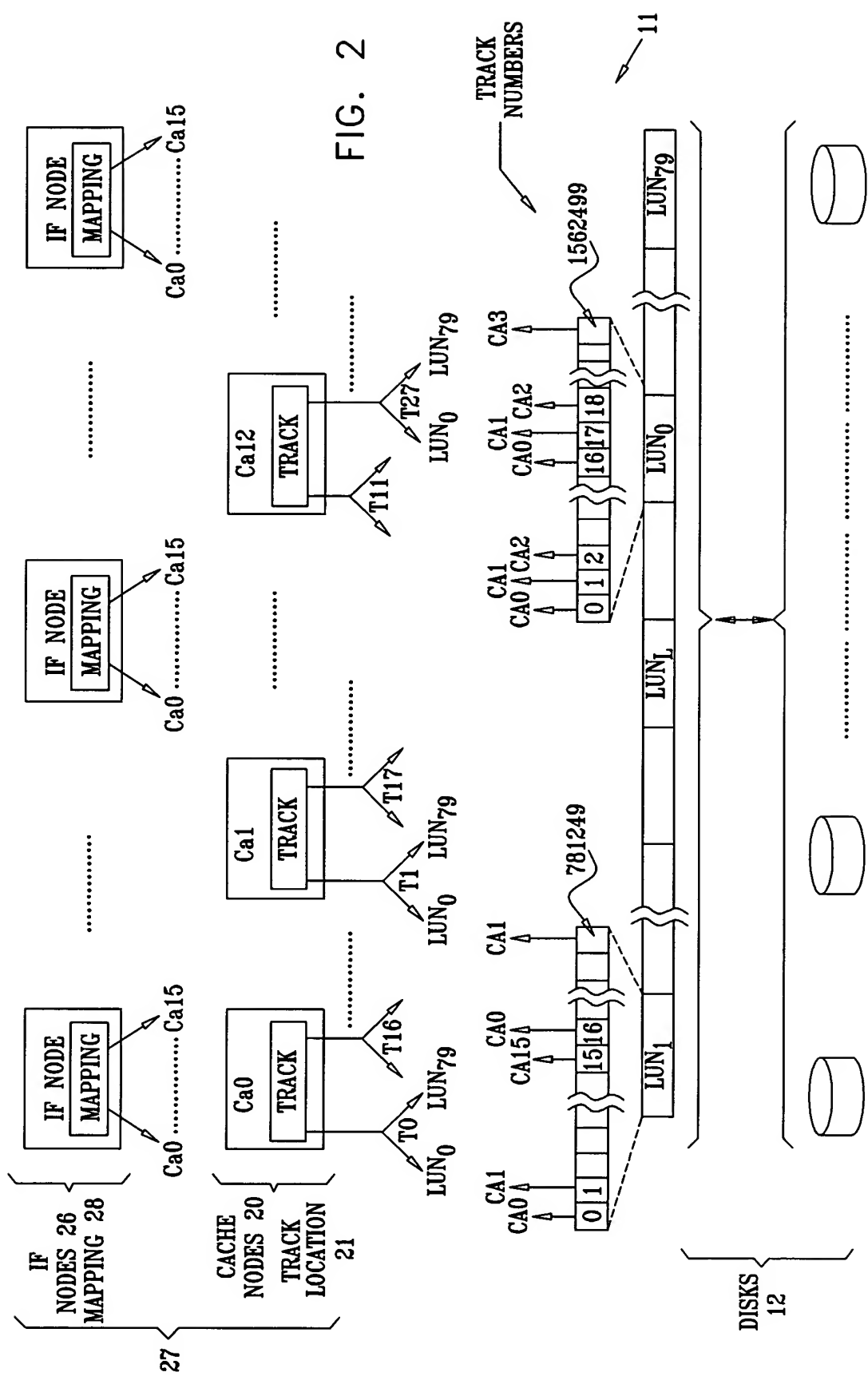


FIG. 3

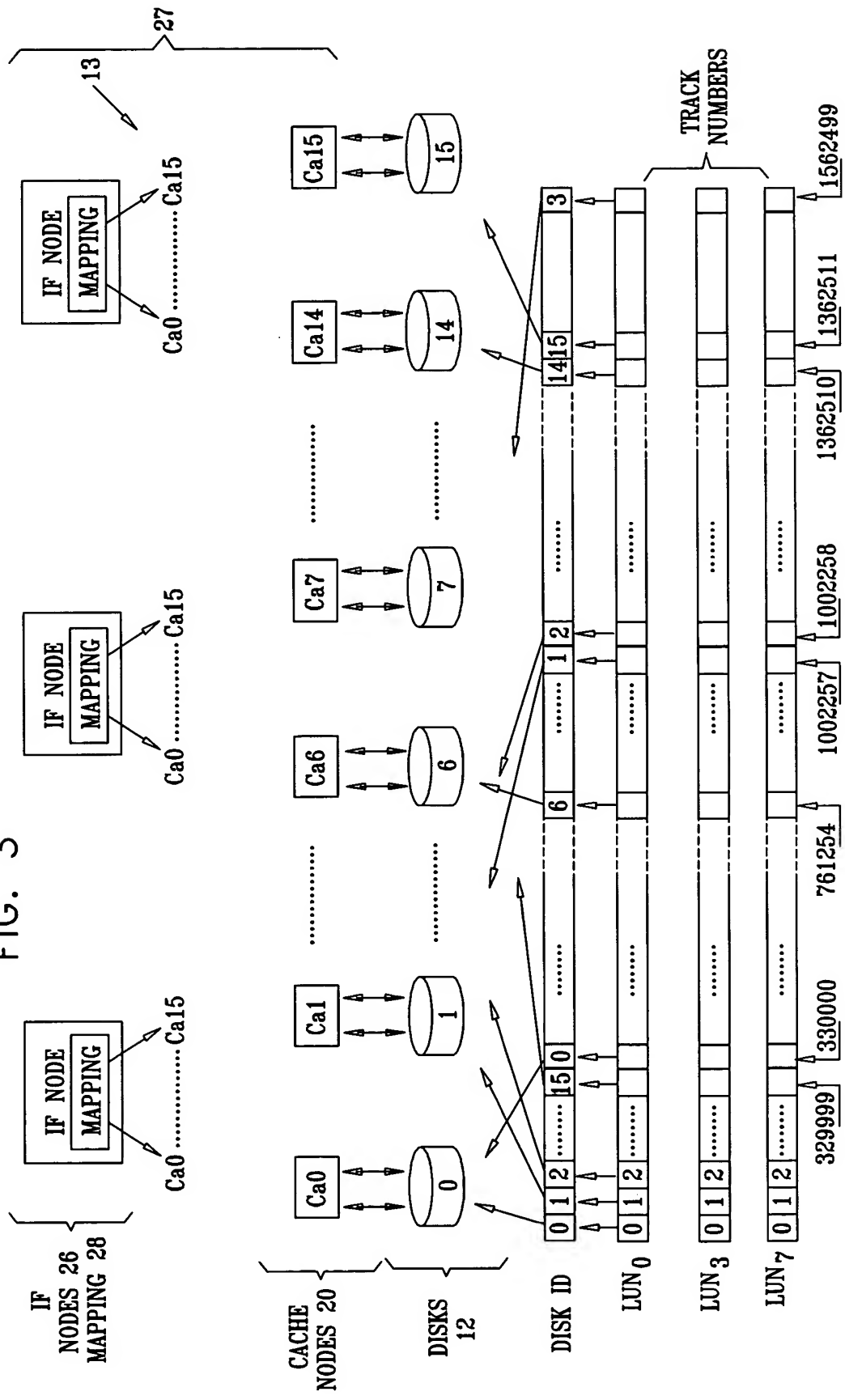


FIG.

The diagram illustrates a storage system architecture. At the top, three "IF NODE MAPPING" blocks are shown, each mapping "Ca0" and "Ca1" inputs. Below these are "CACHE NODES 20 TRACK LOCATION 21", which are mapped to "Ca0" and "Ca1". The central part of the diagram shows a "TRACK" block with "Ca0" and "Ca1" inputs, and a "TRACK NUMBERS" block with "Ca0" and "Ca1" inputs. The bottom section shows a detailed view of "DISKS 12", including "LUN₀" and "LUN₁", with "TRACK NUMBERS" and "LUN₀" labels. The diagram also shows "LUN₀" and "LUN₁" blocks, and a "TRACK NUMBERS" block. The diagram is labeled "FIG." at the top.

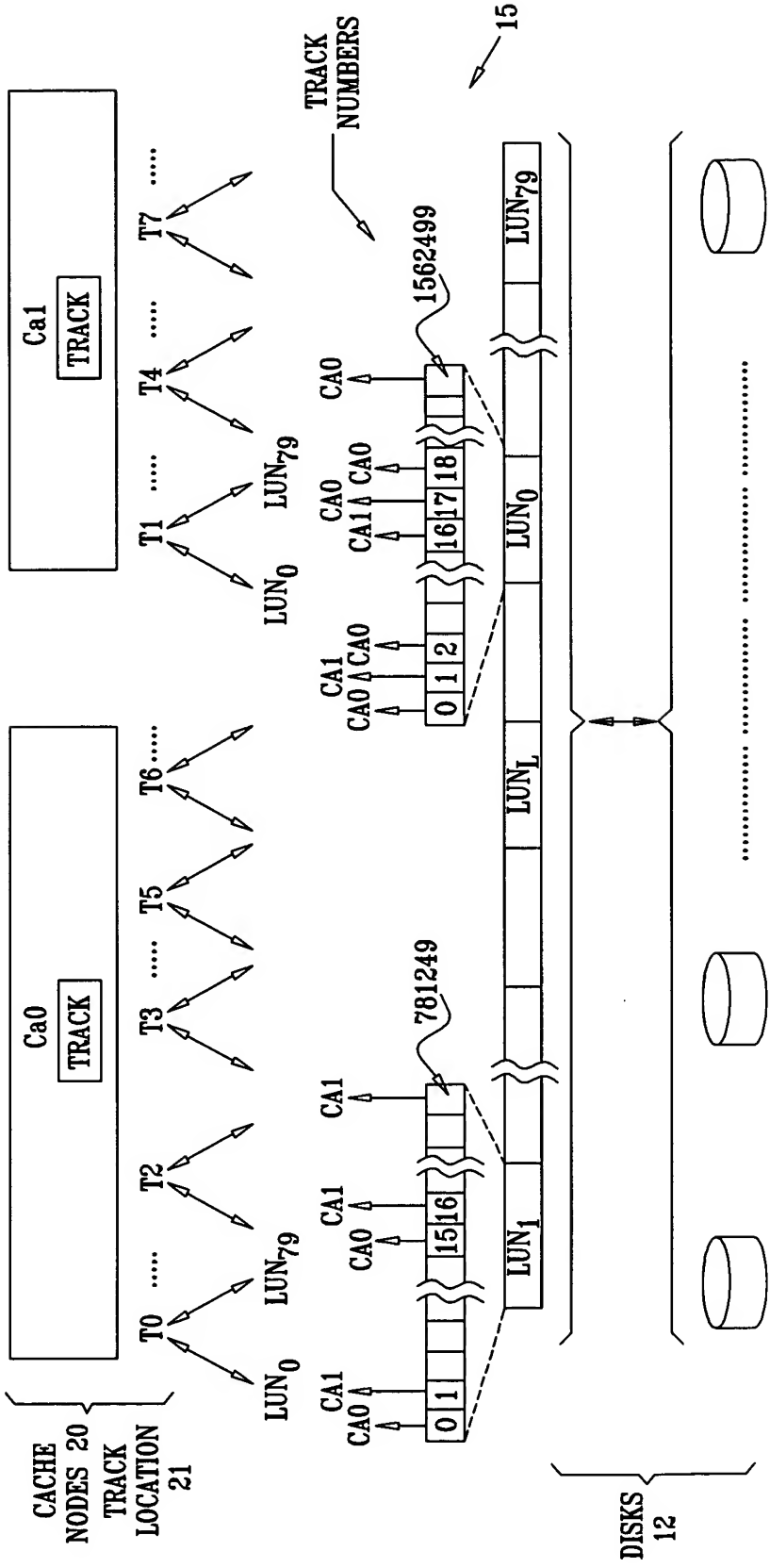


FIG. 5

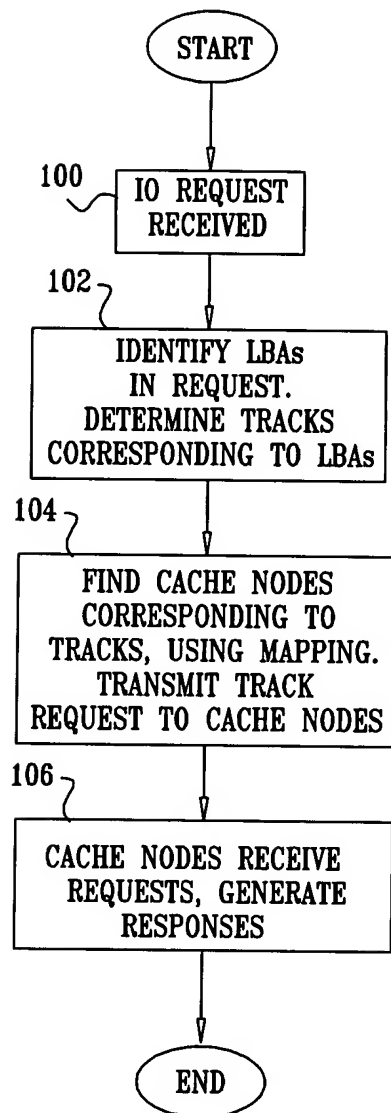


FIG. 6

